



Academic Wing of
**Centre for Development
of Advanced Computing,
(C-DAC)**
Thiruvananthapuram

Live Projects
Placements
Paid Internships

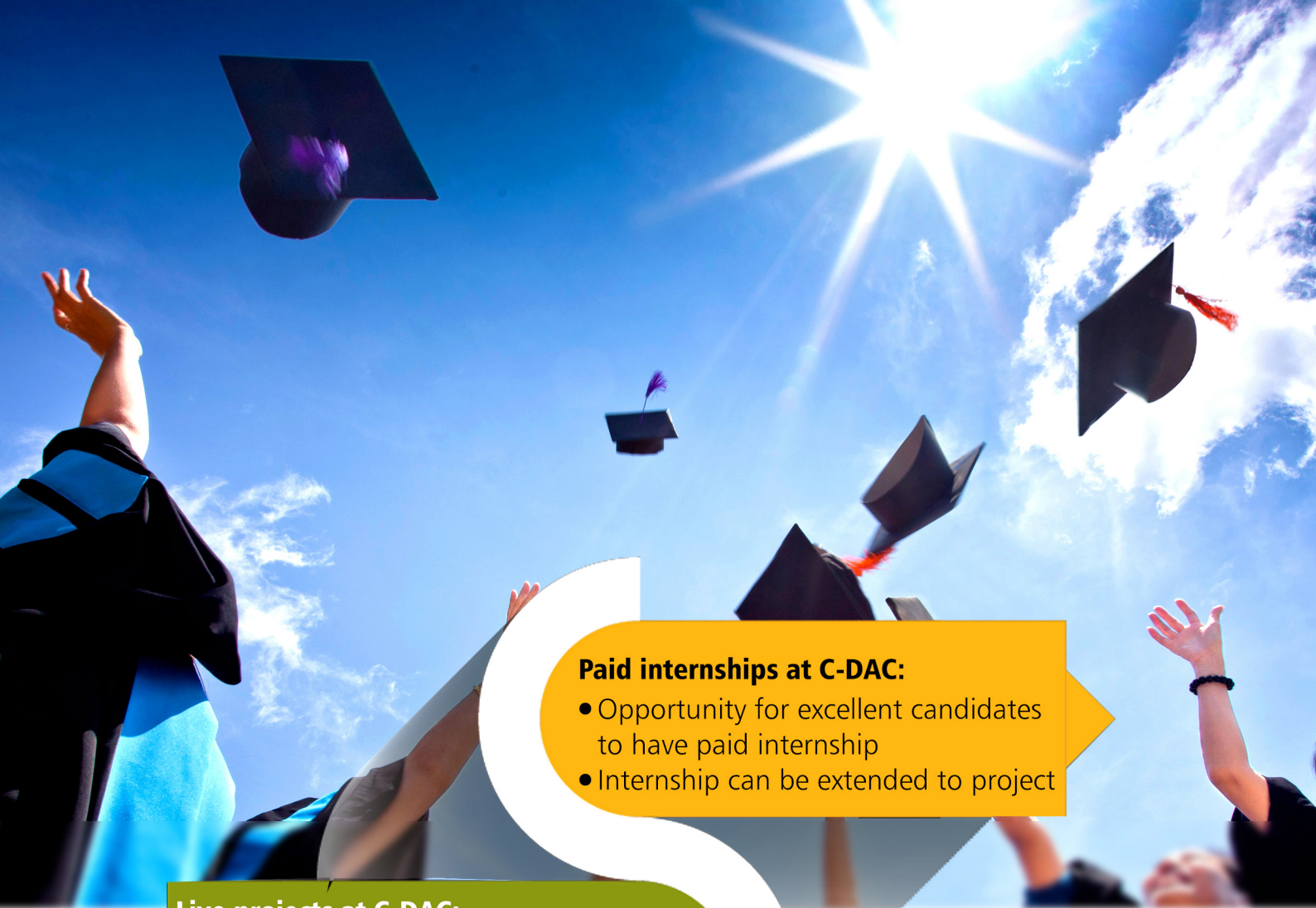
PROSPECTUS



Admission to
**Post Graduate Degree
Programmes in Engineering 2025**



ER&DCI Institute of Technology
Thiruvananthapuram



Paid internships at C-DAC:

- Opportunity for excellent candidates to have paid internship
- Internship can be extended to project

Live projects at C-DAC:

- Opportunity to work in India Microprocessor initiative
- Cyber forensics and cyber security systems

Placements at C-DAC:

- Excellent students will be absorbed in C-DAC as a Project Engineer

Faculties:

- Scientists from C-DAC





Introduction

Centre for Development of Advanced Computing (C-DAC) is the premier Research and Development (R&D) organisation of the Ministry of Electronics and Information Technology, Govt. of India for carrying out R&D in Electronics, Information Technology and associated areas. C-DAC has been at the forefront of the Information Technology revolution, constantly working in emerging technologies and innovating and leveraging its expertise, calibre and skill-sets to develop and deploy IT products and solutions for different sectors of the economy.

The institute is approved by All India Council for Technical Education (AICTE) and affiliated to APJ Abdul Kalam Technological University.

ER&DCI Institute of Technology has a team of qualified faculty members who are also involved in R&D activities of C-DAC. Our facilities include air conditioned class rooms, well-equipped laboratories with high-speed Internet connectivity and a well stocked technical library, projects and internships with Industry/C-DAC, an excellent placement record with industry/C-DAC. Exceptional students can also be placed with C-DAC as Project Engineers after the M.Tech. programme.

Candidates are requested to go through this prospectus carefully. Candidates are also requested to visit the official web site of ER&DCI Institute of Technology at <https://www.erdciit.ac.in> regularly for further information including notifications and announcements related to admission.



About ER&DCI Institute of Technology

ER&DCI Institute of Technology, the Academic arm of Centre for Development of Advanced Computing (C-DAC), Ministry of Electronics & IT, Government of India, offers Post Graduate Degree Programmes in Engineering (M.Tech.) at C-DAC, Vellayambalam Campus, Thiruvananthapuram. The programmes are full time of 24 months duration comprising of 4 semesters.



2 Year Fulltime MTech Programmes Offered

Department	Stream	Prerequisite	Total Number of seats	Number of sponsored seats*
Electronics & Communication Engineering	VLSI and Embedded Systems	BE/B.Tech. Degree in Electronics & Communication/ Applied Electronics & Instrumentation/ Electronics & Biomedical/Biomedical/Instrumentation & Control/ Electronics & Computer / Electrical & Electronics/ Electrical & Computer Engineering OR MSc. Electronics/ Physics.	30	2 (out of 30 seats)
Computer Science & Engineering	Cyber Forensics and Information Security	BE/B.Tech.Degree in Computer Science & Engineering/AI /AI and Data Science/ AI & Machine Learning/ Computer Science & Business Systems/ Computer Science & Design/ Computer Science & Engineering (Cyber Security/ AI and Machine Learning/ AI/Data Science/Digital Forensics & Cyber Security) /IT/ Electronics & Communication/ Electrical & Electronics/ Applied Electronics & Instrumentation/ Electrical & Computer/ Electronics & Computer/ Computer Science & Engineering & Business Systems OR MCA/ MSc. (Computer Science/ Mathematics/ Statistics)	30	2 (out of 30 seats)

Reservation norms decided by Govt. of Kerala shall apply

*** Sponsored Seats: Sponsored candidate must have at least 2 years of service on the date of application.**

Their employers must issue NOC and relieve them to pursue the program for the full duration of the course.

Candidates from Central/State Govt. Institutions, Public Sector Organizations and reputed Private Organizations/Industry/Teachers sponsored from AICTE recognized Engineering Colleges will be considered in this category.

VLSI and Embedded Systems

Established in 2006, the Department of Electronics, was one of the frontrunners in academia to offer M.Tech. in VLSI & Embedded System, with an objective to churn out High Calibre Hardware Engineers.

The programme mainly focuses on the ASIC/FPGA front end Design and Verification, Physical Design and Verification, Analog Mixed Signal Design and Verification, Embedded System Design and Development, Microcontrollers, Analog and Digital Circuit Design, SoC Design, Hardware Description & Verification Languages, Low Power IC Design, RISC-V based processor design etc. Students also get opportunity to work with the latest Electronic Design Automation tools which is in use in the industry.

The Courses handled by Scientists/Engineers working in the R&D groups of C-DAC & faculty members of ERDCIIT, well equipped laboratories (with Industry standard Design Tools supported with FPGA and Microcontroller development boards), opportunities to do their final year M.Tech. project as live projects in different R&D groups of C-DAC/industry in the areas like IP core development and Verification, Strategic Electronics, Control and Instrumentation, Indigenously developed Vega processors of CDAC, e-mobility etc, paid internship opportunities in C-DAC/industry and possible employment at C-DAC as project engineer after M.Tech., all sets the programme distinctive from that offered by other institutes.

The students have been placed in various reputed companies and organizations every year by providing placement assistance through the placement cell. They also get campus placement in top notch companies like HCL Technologies, Quest Global, Infinicomm, TOSSIL, C-DAC etc. Please refer to the section **Placement** for details.

Cyber Forensics and Information Security

Established in the year 2012, with the objective to cater to the exponentially growing need of trained professionals in the upcoming and nascent field of cyber security, the Department of Computer Science and Engineering, ER&DCI Institute of Technology is one of the very few institutes in the country to offer industry specific programmes in Cyber Forensics and Information Security.

The course is designed to equip students with necessary knowledge and tools to fill the need for educated cyber security and cyber forensics practitioners, investigators and engineers. It also ensures that students have a basic understanding of the legal and regulatory requirements and the standards pertaining to computer security.

Courses are delivered by Scientists/Researchers/Faculty members working in the field of Cyber Forensics. The department frequently conducts seminars and invited talks with the support of experienced resource persons from the industry. Students are exposed to live projects of C-DAC/Industry.

The course has been designed to develop expertise of core technical topics within the area of Cyber Security and Digital Forensics such as Information Security, IT Auditing, Network Security, Penetration Testing, Malware Analysis, File System Forensics, Windows and Linux Forensics, Image and Video Forensics, IT law and ethics.

Students get the opportunity to be placed/do Internship in C-DAC/Industry. They also get campus placement in top notch companies like Deloitte, Saint Gobain, UST Global, Tata Elxsi, Reliance, GMR, H&R Block etc. Please refer to the section **Placement** for details. There are opportunity for students to join as Interns from 3rd semester onwards .

Distribution of Seats

General	14	SC/ST	3	SEBC	8	Sponsored	2	EWS	3
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Eligibility for Admission

Eligibilities for admission to Post Graduate Degree Programmes offered at ER&DCI Institute of Technology are given below.

1. The candidates shall be an Indian National
2. The candidates should have studied the graduate degree programme in Engineering in an institute approved by the AICTE or in a Deemed University in India
3. Candidates should have a minimum of 60% aggregate marks in the Engineering Degree/MCA/MSc. examination. For SEBC(OBC) students, a minimum of 55% aggregate marks in the Engineering Degree/MCA/MSc. examination is mandatory. For SC/ST candidates a pass in the Engineering Degree/MCA/MSc. course is sufficient.
4. Candidates, who have passed AMIE/AMIETE examinations, are eligible for admission, provided they have a valid GATE score and a minimum of 55% marks for section B in AMIE/AMIETE examination.
5. Candidates who have appeared for the final semester examination can also apply, provided he/she has passed all the subjects up to and including the 6th semester exam. Confirmation of admission of such candidates shall be subject to the production of qualifying degree before the date stipulated by the affiliated University.



Selection Procedure

1. Applicants shall submit duly filled application form downloaded from the website of the institute.
2. Preference shall be given to candidates who have a valid GATE score and seats shall be filled based on merit (GATE Score Rank).
3. In case, more than one candidate having the same GATE score, they will be ranked based on their marks (or grades) obtained in their qualifying examinations (B.E./B.Tech.)
4. If the seats are vacant due to lack of candidates with valid GATE score, the remaining seats are allotted to candidates who do not have valid GATE score, based on their mark (or grade) in Engineering degree examination upto sixth semester.
5. After the last date of receiving applications, ER&DCI Institute of Technology shall publish the list of selected candidates and the list of wait-listed candidates for each programme. Reservation as specified by Govt. of Kerala is applicable in the selection process.
6. In the case of Sponsored Seats, selection shall be based on their marks/grades obtained in their qualifying examinations (B.E./B.Tech.).
7. Vacant Sponsored seats shall be filled with eligible candidates from the general list
8. The date of counselling/admission shall be published on the institute website. All are required to attend the counselling and admission in person on the day specified along with the original mark lists, Certificates and other necessary documents as listed in this Prospectus. **Individual call letter will not be sent to the candidates.** Counselling will be done based on the selection list published on the institute website.
9. Selected candidates shall have to report at ER&DCI Institute of Technology on the prescribed date and complete the admission process, failing which the seats will automatically be transferred to the next eligible candidates in the waiting list.
10. In case seats remain vacant due to lack of eligible candidates even after considering candidates without valid GATE score, one or more rounds of spot admission / walk-in admission will be conducted to fill the seats.



Submission of Application

Application form for M.Tech. admission can be downloaded from the web site of ER&DCI Institute of Technology (<https://www.erdciit.ac.in>). Candidates have to take printouts of the downloaded application form and fill up all details. Copies of relevant certificates along with the completed application forms should reach in both hard & soft copy by stipulated time.

- Soft copy Submission: Duly filled application form, necessary documents & proof of fee payment (all in originals) should be scanned as separate files in pdf format as listed below and send to erdciit@cdac.in on or before the stipulated deadline dates

SI No	Particulars	Format and maximum size.
1.	Completed Application Form & Payment details	As a single Pdf
2.	Degree certificate or provisional degree certificate of qualifying examination (if available)	Pdf (upto 1MB)
3.	Mark sheet/grade sheet (6 th Semester) of qualifying examination issued by university	Pdf (upto 1MB)
4.	Final Mark sheets/grade sheets of qualifying examination issued by university	As a single pdf
5.	For general category students having CGPA between 6.0 and 6.5 and SEBC category students having CGPA between 5.4 and 5.9, percentage certificate issued by university showing the equivalent percentage of marks, in case the final grade sheet shows CGPA only and does not show equivalent marks or formula to convert CGPA into marks	Pdf (upto 1MB)
6.	GATE score card (For Gate Scorers)	Pdf (upto 1MB)
7.	Secondary School Leaving Certificate	Pdf (upto 1MB)
8.	Certificate showing place of birth (in case birth place has not been indicated in SSLC Certificate)	Pdf (upto 1MB)
9.	Electoral id/Aadhaar card/passport/nativity certificate issued by revenue authority as proof of address	Pdf (upto 1MB)
10.	For SEBC category students, recent caste and non-creamy layer certificate issued by Tahsildar/Village officer	Pdf (upto 1MB)
11.	For SC/ST category students, caste certificate issued by Tahsildar/Village officer	Pdf (upto 1MB)
12.	No objection certificate from employer, if the candidate is employed	Pdf (upto 1MB)
13.	Certificate issued by competent authority showing percentage of disability, if the candidate is differently abled. (Ref: DoPT OM No.36035/3/2004-Esst (Res), dated 29.12.2005)	Pdf (upto 1MB)
14.	For EWS from unreserved category students, EWS certificate from Village Officer/Tahsildar.	Pdf (upto 1MB)

- Hard Copy Submission: Hard copies of the completed application form (Original) and relevant Certificates/documents (self-attested copies) along with payment details should be sent to ER&DCI Institute of Technology by post so as to reach before last date.
- ER&DCI Institute of Technology shall not be responsible for delay in the receipt of application due to any postal or other unforeseen delay.
- Application Form Fee: Each application has to be accompanied by an application fee of Rs **500** by way of crossed demand draft drawn on any nationalized / scheduled bank in favour of C-DAC payable at Thiruvananthapuram. Alternatively the application fee may be transferred through NEFT. In such cases, the NEFT transaction id shall be clearly mentioned in the application form.
- Application fee shall not be refunded under any circumstances.
- Our Account details for NEFT transactions:

Name of Beneficiary: **C-DAC, Thiruvananthapuram**

Bank: **Canara Bank**

Branch: **C-DAC Campus Branch, Vellayambalam**

Account no: **40192200006044**

IFSC Code: **CNRB0014019**

Applications should be complete in all respects. Application forms not accompanied by Demand Draft/Proof of payment through NEFT for application fee and self-attested copies of specified documents shall be considered incomplete and rejected. Applications received after the specified last date shall also be rejected.



Programme Fee

Particulars	I Semester	II Semester	III Semester	IV Semester
Tuition Fee	68430	66900	66900	66900
University Student Affiliation Fee*	1050			
Caution Deposit	6000			
PTA	2000			
Admission fee	1000			
University Student Administration Fee*	1000			
University Sports & Arts Fee*	530		530	
Exam Fee*	2145	2145	1035	2100
Total	82205	69045	68465	69000

***Subject to change if APJ Abdul Kalam Technological University decides to modify any fee**

First semester fee has to be paid at the time of admission. In the subsequent semesters, programme fee has to be paid at the beginning of each semester.



Scholarships, Student projects, Internships and Placements

Scholarship is available for candidates with valid GATE Score as per AICTE norms for the total period of the programme.

Students of the institute are offered opportunity to do their student projects (third and fourth semesters) in various groups of C-DAC and industry to get involve in live projects. This will enable the students to get first-hand knowledge in state-of-the-art technologies and in industry work culture and improve their employability.

Internships are stepping stones for a student to his future professional career. It allows the student to gain valuable exposure to the workplace, provides the opportunity for skill development, and gives a competitive edge in the job search. Eligible students of the institute shall have the opportunity to do paid internship with C-DAC Thiruvananthapuram during the course. This facility depends upon the publication of second semester results, vacancies available and is limited to students with good academic track record.

ER&DCI Institute of Technology provides a strong base for placement and industrial training requirements of students. There is a dedicated placement cell working in the institute with the objective to provide students employment opportunity in leading organizations. Alumni from the institute are working in highly acclaimed companies including Saint-Gobain, TataElxsi, Wipro, Infosys, UST Global, HCL Technologies, IBS, TCS, EY, Deloitte, GMR and government organizations including C-DAC, ADA, NIC, AAI, ISRO, Railways and as security specialist officers in nationalized and scheduled banks.

Batch	2020-2022	2021-23	2022-24
VLSI & Embedded Systems	100%	100%	80%
Cyber Forensics & Information Security	78%	100%	88%

Prevention and Prohibition of Ragging

Ragging is totally banned in ER&DCI Institute of Technology premises and anyone found guilty of ragging and/or abetting ragging is liable to be punished. In case, the applicant for admission is found to have indulged in ragging in the past or if it is noticed later that he/she has indulged in ragging, admission may be refused or he/she shall be expelled from the institution.

Each of the students of the institution and his/her parent or guardian is required to submit an undertaking in this regard at the time of admission in the prescribed format which is mandatory for admission. The formats of these affidavits are available for download at our web site.

IMPORTANT DATES*

Starting date of receiving application	14-05-2025
Closing date of application	30-06-2025
Publication of Draft rank list	03-07-2025
Closing date for receiving hard copies of the application forms	07-07-2025
Publication of final rank list	04-07-2025
First allotment list publication	08-07-2025
Joining dates for first allotment	14-07-2025 & 15-07-2025
Second allotment list publication (Tentative)	16-07-2025
Joining dates for second allotment	21-07-2025 & 22-07-2025
Commencement of regular class/orientation class	Shall be announced later as per the notification from University

* In the event of date been declared as a holiday, the next working day will be treated as the date.

Contact



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E-mail: erdciit@cdac.in, Website: <https://www.erdciit.ac.in>